Application Number 10/036,622 Responsive to Office Action mailed July 22, 2005

AMENDMENTS TO THE SPECIFICATION

Please replace the section entitled CROSS REFERENCE TO RELATED APPLICATIONS, starting at page 1 line 7 through line 25, with the following amended paragraphs:

CROSS-REFERENCE TO RELATED APPLICATIONS

This Application is related to the following Applications:

"Cross-Bar Switch Supporting Implicit Multicast Addressing," by Nazar Zaidi, Mark Bryers, and Abbas Rashid, Attorney-Docket No. NEXSI-01221US0, filed the same day as the present application U.S. Patent Application Serial Number 10/036,603, filed on December 21,2001; and

"Cross-Bar Switch With Explicit Multicast Support," by Abbas Rashid, Mark Bryers, and Nazar Zaidi, Attorney Docket No. NEXSI-01222USO, filed the same day as the present application U.S. Patent Application Serial Number 10/036,602, filed on December 21, 2001; and

"Cross-Bar Switch Incorporating A Sink Port With Retry Capability," by Mark Bryers, Abbas Rashid, and Nazar Zaidi, Attorney Docket No. NEXSI 01223US0, filed the same day as the present application U.S. Patent Application Serial Number 10/036,762, filed on December 21, 2001;

"Cross-Bar Switch with Bandwidth Allocation," by Mark Bryers, Fred Gruner, Abbas Rashid, and Nazar Zaidi, Attorney Docket No. NEXSI-01223US0, filed the same day as the present application U.S. Patent Application Serial Number 10/036,595, filed on December 21, 2001; and

"Cross-Bar Switch With Sink Port Accepting Multiple Packets," by Abbas Rashid, Mark Bryers, and Nazar Zaidi, Attorney Docket No. NEXSI-01226US0, filed the same day as the present application U.S. Patent Application Serial Number 10/037,144, filed on December 21, 2001.

Each of these related Applications is are incorporated herein by reference.

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In addition, please replace the last paragraph on page 6, with the following amended paragraph:

DTE is a generic name for a computing system including a processing engine, ranging from a complex multi-processor computer system to a stand-alone processing engine. At least one example of a DTE is a multi-processor unit 10 described in U.S. Patent Application Ser No. 09/900,481, entitled Multi-Processor System, filed on July 6, 2001, having Attorney Docket No. NEXSI 01020USO, and U.S. Patent No. 6,839,808, hereby incorporated by reference.